

A copy of the Information Disclosure Statement ("IDS") (PTO/SB/08A) accompanies this Amendment. The IDS and copies of all of the cited references are being sent on Thursday, February 7, 2002 via U.S. Postal Service Express Mail Label EV067266141US to:

Honorable Commissioner for Patents
Box IDS
Washington, D.C. 20231
ATTN: Examiner Roy K. Potter, Group Art Unit 2822

SUMMARY

In light of the above amendments, early examination is earnestly solicited. Should the Examiner have any questions, comments or suggestions in furtherance of the prosecution of this application, the Examiner is invited to contact the attorney of record by telephone or facsimile. If there are any fees due with the filing of this Response, including any fees for an extension of time, Applicants respectfully Petition the Commissioner for such an extension and direct that any and all fees be charged to Baker Botts L.L.P., Deposit Account Number 02-0383 (formerly Baker & Botts, L.L.P.), Order Number 068354.0161.

Respectfully Submitted,

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February 7, 2002

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AMENDMENT VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the Title:

At page 1, beginning at line 1, the title was amended as follows:

An Integrated Circuit (IC) Package With A Microcontroller Having An N-Bit Data
Bus Width With Less Than N I/O Pins And A Method Therefor
Up To N-Pins Coupled To The Microcontroller

In the Specification:

At page 1, lines 5-7, the following paragraph was amended as follows:

RELATED APPLICATIONS

This application is a continuation of commonly owned Divisional Application of U.S. patent application Serial Number 08/644,916, filed May 24, 1996, in the name of the same inventors as the present patent application now United States Patent Number 5,847,450, issued December 8, 1998.

09522026-031402

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